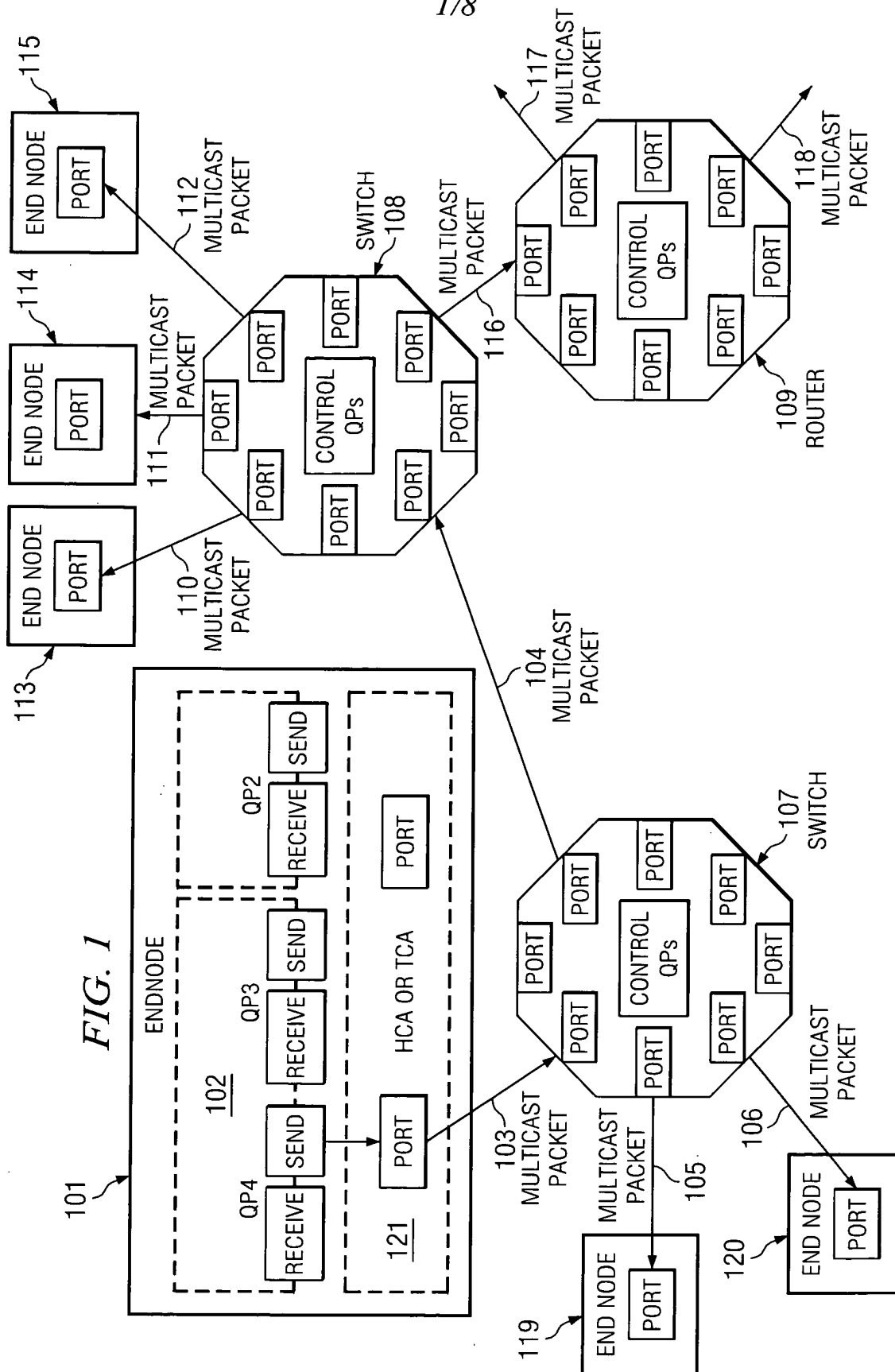


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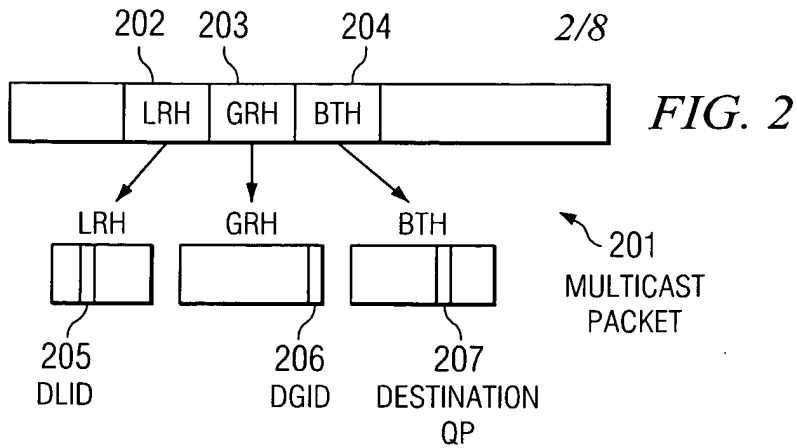
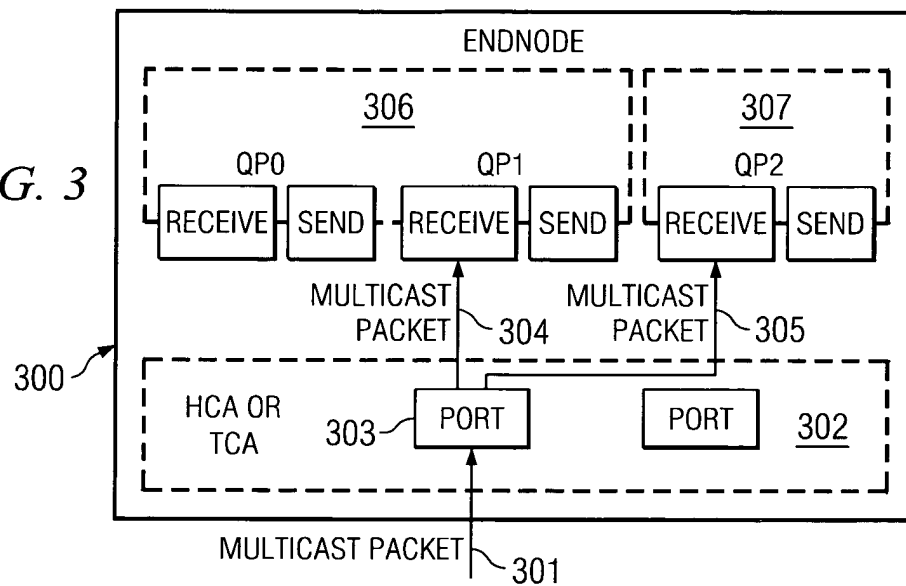
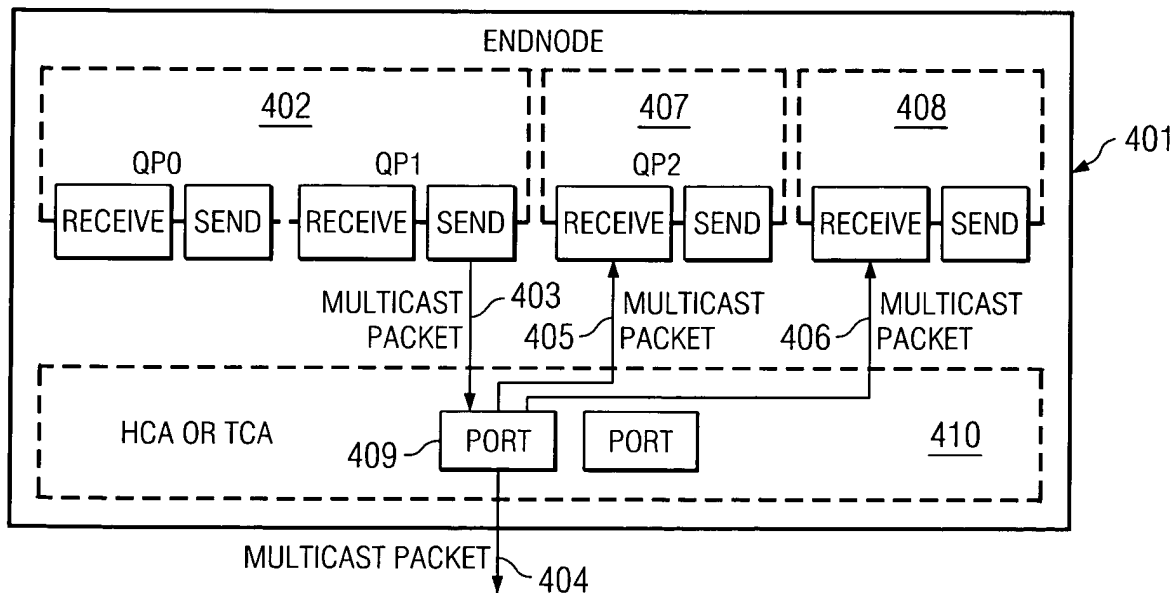
**FIG. 3****FIG. 4**

FIG. 5

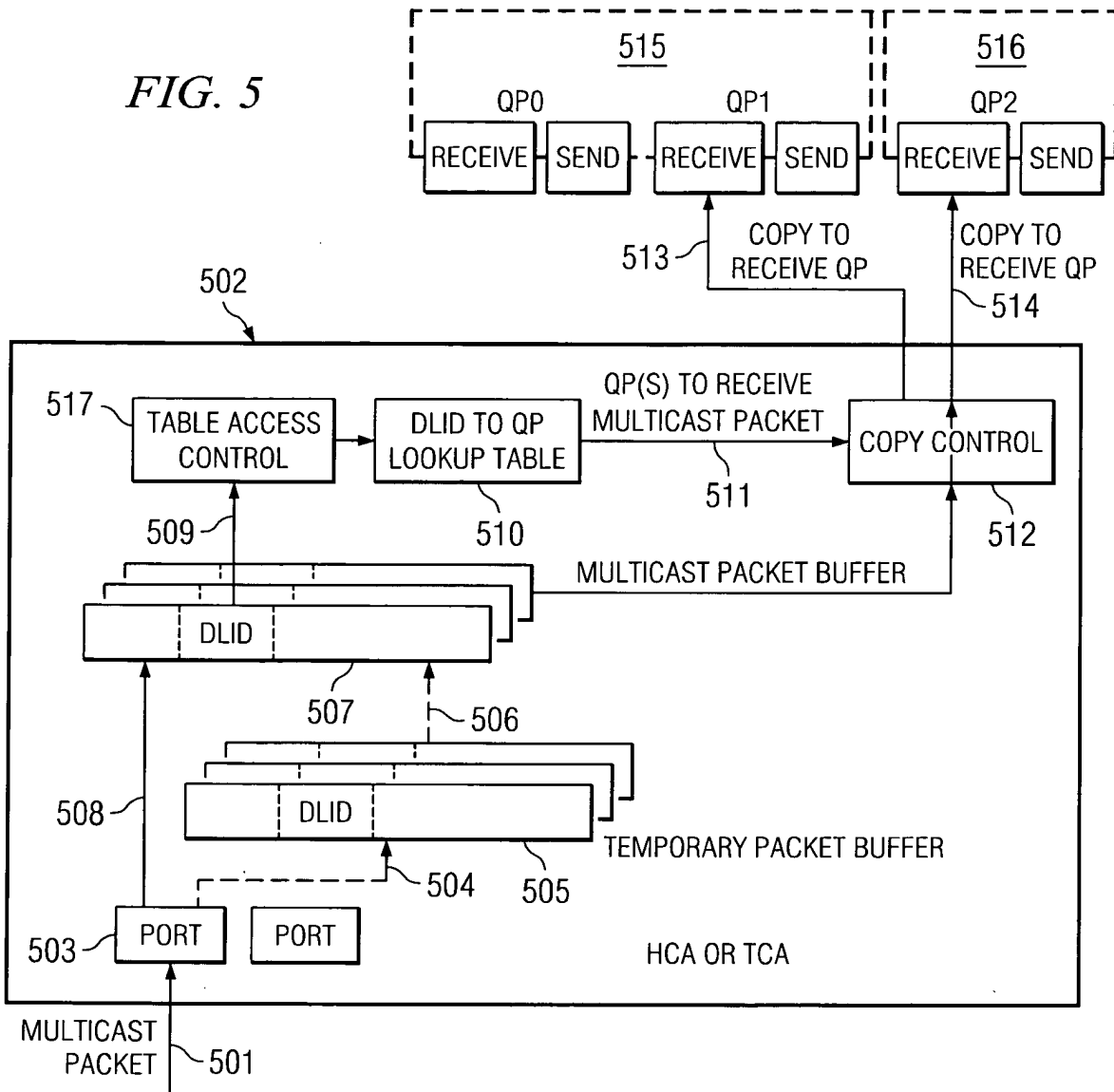
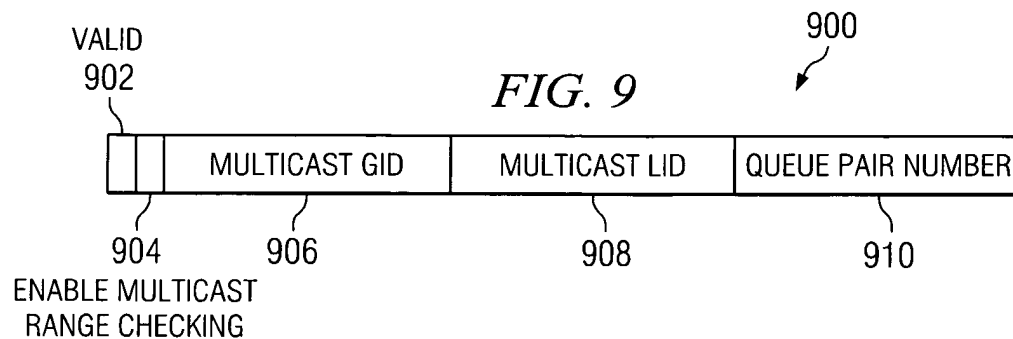
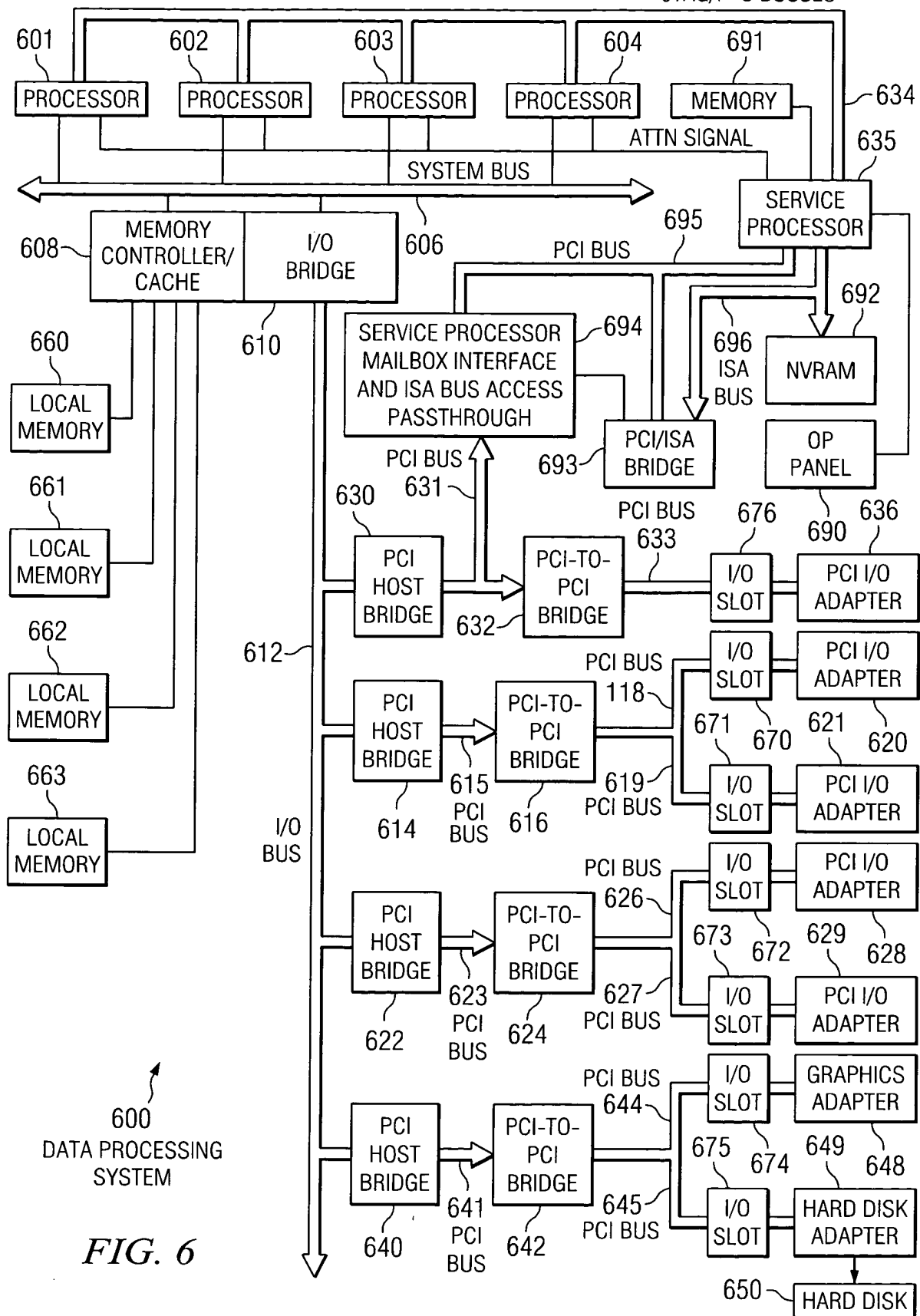


FIG. 9



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JTAG/I²C BUSES

600
DATA PROCESSING
SYSTEM

FIG. 6

LOGICALLY PARTITIONED
PLATFORM

700

FIG. 7

